

XEM6110 Product Change Notice (PCB Datecode 20120307)

PCB datecode 20120307 introduced the following changes to the XEM6110 product. These changes were made to improve timing closure in the Xilinx tools as well as improve signal integrity on-board. The new product is known as the XEM6110v2. The first-generation device (XEM6110) will no longer be available.

- okGH[2] moved from FPGA pin location AA12 to W12.
- okGH[3] moved from FPGA pin location AB12 to Y12.
- okHG[0] moved from FPGA pin location W12 to W8.
- okHG[1] moved from FPGA pin location Y12 to V7.
- Several expansion connector signal lengths changed. Please see the latest revision of the *XEM6110 User's Manual* for a detailed list.
- New board identification codes `brdXEM6110v2LX45` and `brdXEM6110v2LX150`.

Mechanical Changes

There are no mechanical changes to the footprint, connector locations, or connector pinout.

Electrical Changes

Several expansion connector signal lengths changed. Please see the latest revision of the *XEM6110 User's Manual* for a detailed list.

Design Changes

The latest copy of the FrontPanel SDK is recommended. Assets associated with the new XEM6110v2 product are available in FrontPanel 4.1.0 and later.

Due to the movement of four host interface pins, bitfiles generated for the XEM6110 will not be compatible with the XEM6110v2. The new board identification codes may be used to discriminate between these two products and select the appropriate bitfile.